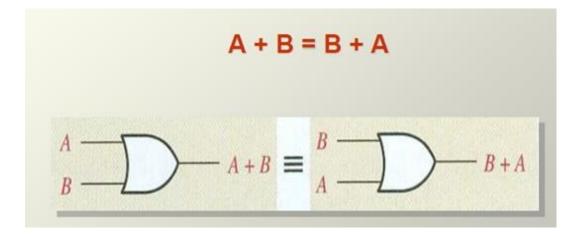
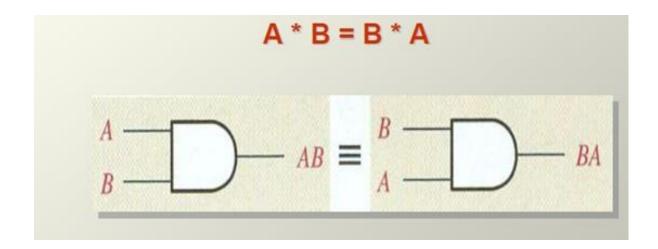
Logical Design Lecture 10

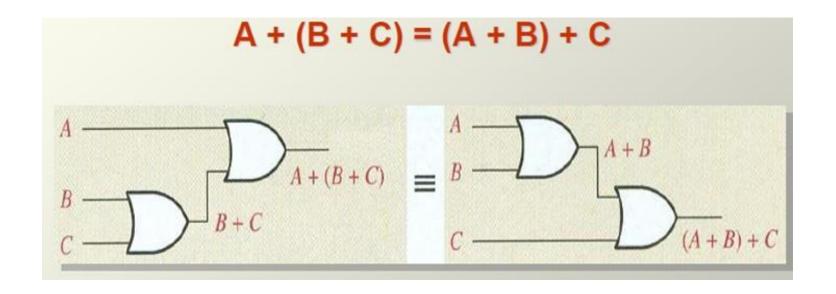
Laws of Boolean Algebra

Commutative Law

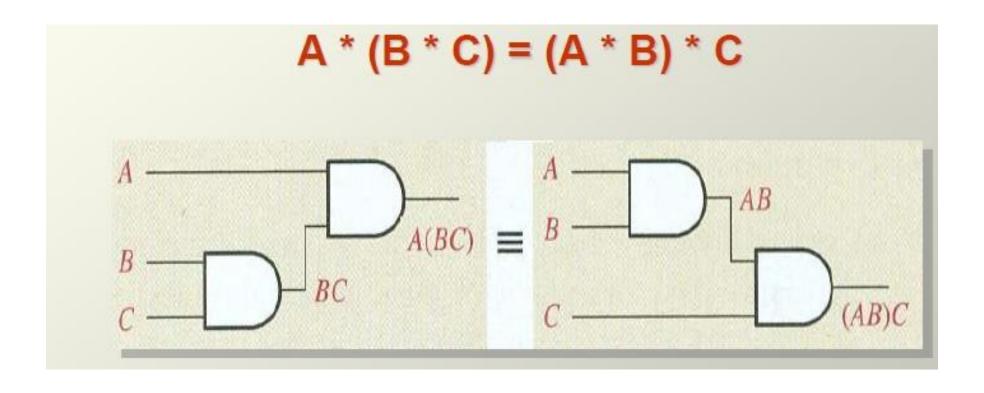




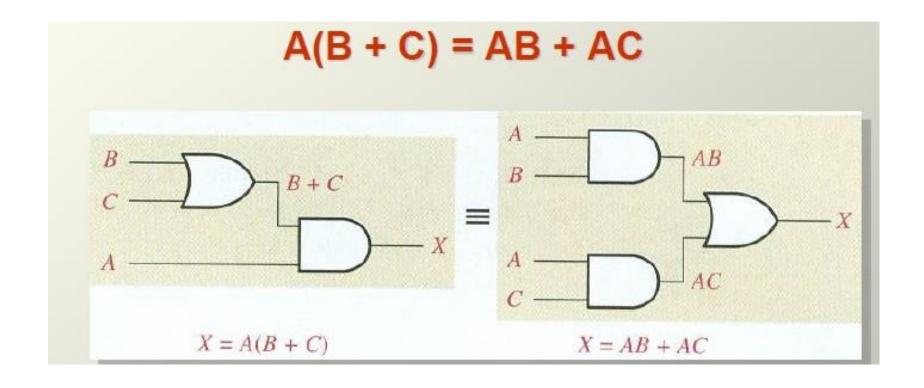
Associative Law (Addition)



Associative Law (Multiplication)



Distributive Law



Rule 1 Identity (ADDITION): X=A+0=A

$$A = 1$$

$$0$$

$$X = 1$$

$$0$$

$$X = 0$$

$$0$$

$$X = 0$$

Rule 2 NULL (ADDITION): X=A+1=1

$$A = 1$$

$$1$$

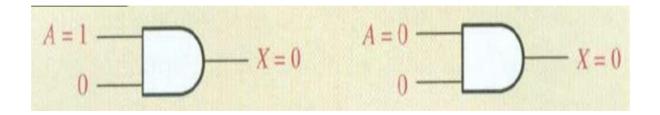
$$X = 1$$

$$1$$

$$X = 1$$

Α	В	X
0	0	0
0	1	1
1	0	1
1	1	1

Rule 3 NULL (MULTIPLICATION): X=A*0=0



Rule 4 Identity (MULTIPLICATION): X=A*1=A

A	В	X
0	0	0
0	1	0
1	0	0
1	1	1

$$\begin{array}{c|c}
A = 0 & & & A = 1 \\
\hline
 & & & & & \\
1 & & & & & \\
\end{array}$$

$$X = 1$$

Rule 5 Idempotent (ADDITION): X=A+A=A

$$A = 0$$

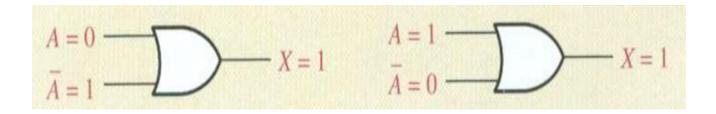
$$A = 0$$

$$X = 0$$

$$A = 1$$

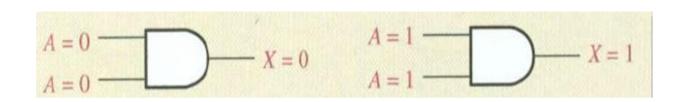
$$A = 1$$

Rule 6 Complementary (ADDITION): $X=A+\overline{A}=1$



A	В	X
0	0	0
0	1	1
1	0	1
1	1	1

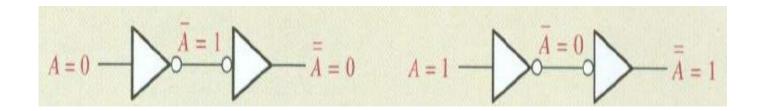
Rule 7 Idempotent (MULTIPLICATION): X= A*A=A



Rule 8 Complementary (MULTIPLICATION): $X = A*\bar{A}=0$

$$A = 1$$
 $\overline{A} = 0$
 $X = 0$
 $\overline{A} = 1$
 $X = 0$
 $X = 0$

Rule 9 Involution: $A=\bar{A}$



Rule 10 OR Absorption: $A + (A \cdot B) = A$

A	В	AB	A + AB	$A \rightarrow \bigcirc$
0	0	0	0	
0	1	0	0	$B \longrightarrow$
1	0	0	1	
1	1	1	1	A straight connection
†			A	

The proof: Left side = A + AB

= A (1+B)

=A•1

=A

Rule 11 AND Absorption: $A \cdot (A + B) = A$

The proof:

Left side = A(A+B)

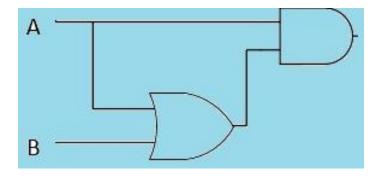
 $=A \cdot A + AB$

= A + AB

= A(1 + B)

=A•1

=A



Α	В	A+B	A.(A+B)
0	0	0	0
0	1	1	0
1	0	1	1
1	1	1	1

Rule 12 Redundancy: $A + \overline{A} B = (A + B)$

The proof:

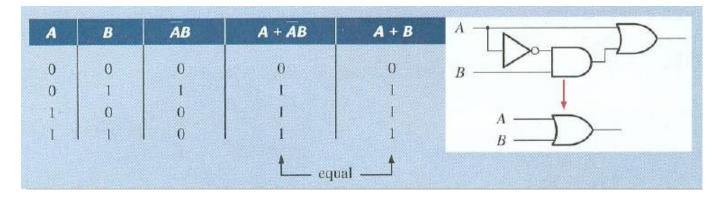
Left side = $A + \bar{A}B$

$$= (A + \overline{A}) (A+B)$$

$$= 1 * (A+B)$$

=A+B

= Right side



Rule 13:
$$(A + B)(A + C) = A + BC$$

The proof:

Left side =
$$(A+B)(A+C)$$

$$= AA + AC + AB + BC$$

$$= A + AC + AB + BC$$

$$= A(1 + C + B) + BC$$

$$= A + BC = Right side$$

0 0 0 0 0 0 0 0 1 0 1 0 0 0 0 1 0 1 0 0 0 0 0 1 1 1 1 1 1 1 1 0 0 1 1 1 1 0 1
0 1 0 1 0 0 0 1 0 1 0 0 0 0 1 1 1 1 1 1 0 0 1 1 1 1 1
1 0 1 0 0 0 0 1 1 1 1 1 1 1 0 0 1 1 1 0 1
1 1 1 1 1 1 1 0 0 1 1 1 1 0 1
0 0 1 1 1 0 1
0 1 1 1 1 0 1
1 0 1 1 0 1

DeMorgan's Theorem (First Theorem)

The complement of a product of variables is equal to the sum of complemented variables.

$$\overline{AB} = \overline{A} + \overline{B}$$

Applying DeMorgan's First Theorem to gates:

$$\begin{array}{ccc}
A & & & \\
B & & & \\
\hline
NAND & & \\
\end{array}$$
Negative-OR

Α	В	~ A	~ B	~ AB	~ A + ~B
0	0	1	1	1	1
0	1	1	0	1	1
1	0	0	1	1	1
1	1	0	0	0	0

DeMorgan's Theorem (Second Theorem)

The complement of a sum of variables is equal to the product of complemented variables. $\overline{A+B}=\overline{A}\bullet\overline{B}$

Applying DeMorgan's Second Theorem to gates:

$$\begin{array}{c}
A \\
B
\end{array}$$
NOR
$$\overline{A+B} \equiv A \longrightarrow B \longrightarrow \overline{AB}$$
Negative-AND

Α	В	~ A	~ B	~ (A+B)	~ A . ~B
0	0	1	1	1	1
0	1	1	0	0	0
1	0	0	1	0	0
1	1	0	0	0	0

Example: Apply DeMorgan's theorem to simplify the following expression.

$$X=\bar{c}+D$$

To apply DeMorgan's theorem to the above expression, we can break the over-bar that covering both terms and change the sign between them as follows:

$$X = \overline{\overline{C}} \cdot \overline{D}$$

Example: Simplify the following expression

$$Y = (A+B)(A+\overline{B})$$

$$= AA + A\overline{B} + AB + B\overline{B}$$

$$= A + A(\overline{B} + B)$$

$$= A+A$$

$$= A$$

OR:

$$Y = AA + A\bar{B} + AB + B\bar{B}$$

 $= AA + A\bar{B} + AB$
 $= A + A\bar{B} + AB$
 $= A(1 + \bar{B} + B)$
 $= A \cdot 1$
 $= A$

Example:

$$Y = ABC + A\bar{B}C + AB\bar{C}$$

$$=AB(C + \bar{C}) + A\bar{B}C$$

$$= AB + A\bar{B}C$$

$$= A(B + \overline{B}C)$$

$$=A(B+C)$$

Example:

$$Y = ABC + A \bar{B} (\overline{A} \overline{C})$$

$$= ABC + A\bar{B} \ (\bar{\bar{A}} + \bar{\bar{C}})$$

$$= ABC + A\overline{B} (A + C)$$

$$= ABC + A \bar{B} + A\bar{B}C$$

$$= AC(B + \bar{B}) + A\bar{B}$$

$$=\mathsf{AC}+\mathsf{A}\,\bar{B}$$

$$= A (C + \overline{B})$$

Determine if the following equation is valid (Derived from the truth table)

$$\bar{X}1 \bar{X}3+X2 X3+X1 \bar{X}2 = \bar{X}1 X2+X1 X3+ \bar{X}2 \bar{X}3$$

	Left-Hand Side (LHS)						
Row number	x_1	x_2	x_3	$\overline{x_1}\overline{x_3}$	$x_2 x_3$	$x_1 \overline{x_2}$	f
0	0	0	0	1	0	0	1
1	0	0	1	0	0	0	0
2	0	1	0	1	0	Õ	1
3	0	1	1	0	1	ŏ	1
4	1	0	0	0	0	1	1
5	1	0	1	0	0	1	1
6	1	1	0	0	0	0	0
7	1	1	1	0	1	0	1

Row number	x_1	x_2	x_3	$\frac{1}{x_1x_2}$	x_1x_3	$\overline{x_2} \overline{x_3}$	f
0	0	0	0	0	0	1	1
1	0	0	1	0	0	0	0
2	0	1	0	1	0	0	1
3	0	1	1	1	0	0	1
4	1	0	0	0	0	1	1
5	1	0	1	0	1	0	1
6	1	1	0	0	0	ŏ	0
7	1	1	1	0	1	ő	

Simplify the following expression, then draw the logic circuit after simplification.

$$Y = \overline{A + B} + \overline{ABC} + \overline{AB}$$

$$=\bar{A}\ \bar{B}+\bar{A}+\bar{B}+\bar{C}+\bar{A}+\mathsf{B}$$

$$= \bar{A}(\bar{B} + 1 + 1) + (B + \bar{B}) + \bar{C}$$

$$= \bar{A} + 1 + \bar{C}$$

logic circuit= Nothing

For the given truth table simplify and draw the circuit before and after simplification.

$$Y = \bar{A}B\bar{C} + \bar{A}BC + AB\bar{C} + ABC$$

$$= \bar{A}B(\bar{C} + C) + AB(\bar{C} + C)$$

$$= \bar{A}B + AB$$

$$= B(\bar{A} + A)$$

$$= B$$

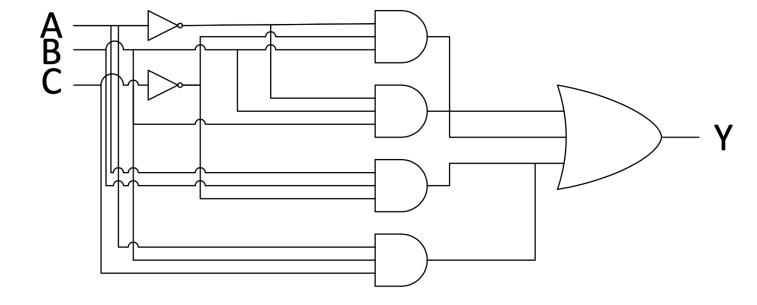
Α	В	С	Υ
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Drawing of the circuit before and after simplification

The circuit after simplification:

В — У

The circuit before simplification:



Sum of Products

The *Sum of Product* (SOP) expression comes from the fact that two or more products (AND) are summed (OR) together. That is the outputs from two or more AND gates are connected to the input of an OR gate so that they are effectively OR'ed together to create the final AND-OR logical output.

$$Q = (A.B) + (\bar{B}.C) + (A.1)$$

However, Boolean functions can also be expressed in nonstandard sum of products forms like that shown below but they can be converted to a standard SOP form by expanding the expression. So:

$$Q = A.\overline{B}(\overline{C} + C) + ABC$$

Becomes in sum-of-product terms:

$$Q = A.\overline{B}.\overline{C} + A.\overline{B}.C + ABC$$

Sum-of-Product Example

The following Boolean Algebra expression is given as:

$$Q = \bar{A}(\bar{B}C + BC + B\bar{C}) + ABC$$

- 1. Convert this logical equation into an equivalent SOP term.
- 2. Use a truth table to show all the possible combinations of input conditions that will produces an output.
- 3. Draw a logic gate diagram for the expression.

Sum-of-Product Example

1. Convert to SOP term

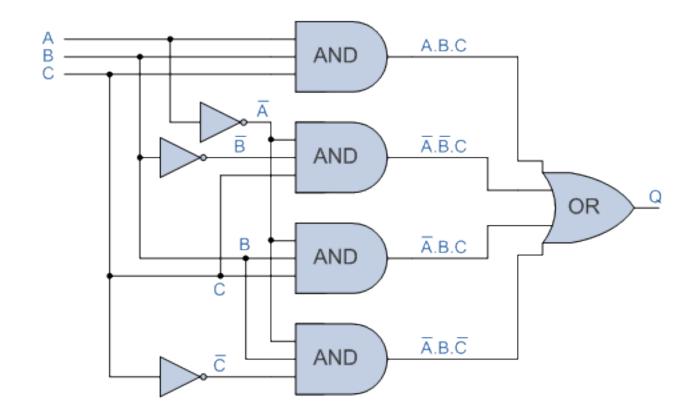
$$Q = A.B.C + \bar{A}.\bar{B}.C + \bar{A}.B.C + \bar{A}.B.\bar{C}$$

2. Truth Table: Sum of Product Truth Table Form

	inputs			Product
Α	В	С	Q	
0	0	0	0	
0	0	1	1	$ar{A}$. $ar{B}$. C
0	1	0	1	$ar{A}$. B . $ar{C}$
0	1	1	1	Ā. B. C
1	0	0	0	
1	0	1	0	
1	1	0	0	
1	1	1	1	A.B.C

Sum-of-Product Example

3. Logic Gate SOP Diagram



Product of Sum (POS)

The *Product of Sum* (POS) expression comes from the fact that two or more sums (OR's) are added (AND'ed) together. That is the outputs from two or more OR gates are connected to the input of an AND gate so that they are effectively AND'ed together to create the final (OR AND) output.

Product of Sum Expressions: Q = (A + B).(B + C).(A + 1)

 $Q = A + (BC) \rightarrow using the distributive law : Q = (A + B)(A + C)$

 $Q = (A + B) + (A.C) \rightarrow Q = (A + B + A)(A + B + C)$

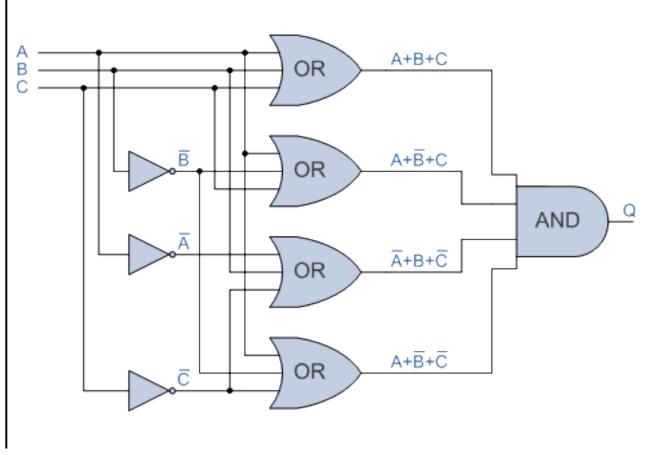
Product of Sum example

The following Boolean Algebra expression is given as:

$$Q = (A + B + C)(A + \bar{B} + C)(A + \bar{B} + \bar{C})(\bar{A} + B + \bar{C})$$

- 1. Use a truth table to show all the possible combinations of input conditions that will produces a "0" output.
- 2. Draw a logic gate diagram for the POS expression.

inputs			output	Product
А	В	С	Q	
0	0	0	0	A+B+C
0	0	1	1	
0	1	0	0	$A + \overline{B} + C$
0	1	1	0	$A + \overline{B} + \overline{C}$
1	0	0	1	
1	0	1	0	$\bar{A} + B + \bar{C}$
1	1	0	1	
1	1	1	1	



Homework

1- Design a logic circuit with three inputs variables (A, B, C) that will produce a HIGH ("1") output if the input is Odd number?

2- Design a logic circuit that has three inputs, *A*, *B*, and *C*, and whose output will be HIGH only when two or more inputs are HIGH.

References

https://whatis.techtarget.com/definition/logic-gate-AND-OR-XOR-NOT-NAND-NOR-and-XNOR

http://www.ee.surrey.ac.uk/Projects/CAL/digital-logic/gatesfunc/#andgate

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https://www.youtube.com/watch?v=JbIBDCTWAnQ