

University of Mosul/ College of Engineering/ Computer Engineering Department

Master's and PhD Thesis – Computer Engineering Department (2008–2013)

No.	Title	Student Name	Supervisor	Degree	Year
1	"Design and Testing of a Reconfigurable Arithmetic Unit Using Field-Programmable Gate Array (FPGA)"	Ihsan Abdul Sattar Ali	Dr. Basel Shukir Mahmoud	M.Sc.	2008
2	Multi-Basis Adaptive Wavelet Network	Ahmed Qasim Ahmed	Mr. Thakwan Mohammed Salim	M.Sc.	2008
3	Restoration of Distorted and Blurred Images Using the Wavelet Transform Method	Akram Abdul Maojoud	Dr. Jasim Mohammed Abdul Jabbar	M.Sc.	2008
4	Design and Implementation of a Self-Tuned Fuzzy Controller Using Virtual Machine Environment	Sara Nawfal Abdul Razzaq	Dr. Fakhr Alddin Hamed Ali	M.Sc.	2008
5	Implementation of Channel Estimation and Multiuser Detection for DS-CDMA System Using FPGA	Dhafer Abdul Fattah	Dr. Ahmed Khorsheed	Ph.D.	2008
6	Retrieval of Impulse-Damaged Images Using Wavelet Transform and Symmetric Reconstruction Techniques	Hiba Nabil Yahya	Dr. Jasim Mohammed Abdul Jabbar	M.Sc.	2008
7	Design and Implementation of the Pipelined Wavelet Transform Network Using FPGA	Zainab Rami Saleh	Dr. Jasim Mohammed Abdul Jabbar	M.Sc.	2009
8	Implementation of the Hardware Architecture of an Edge Extraction Filter Using FPGA	Zahraa Tareq Mohammed	Dr. Fakhruddin Hamed & Dr. Abdul Sattar Mohammed	M.Sc.	2009
9	Design and Implementation of an Embedded System for Intrusion Detection in Wireless Applications	Sahar Lazim Qaddouri	Dr. Qutaiba Ibrahim	M.Sc.	2009

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10	Design and Implementation of a Real-Time Medical Signal Monitoring System Using GPRS	Omar Muzahim	Asst. Prof. Thakwan Mohammed Salim	M.Sc.	2009
11	Design and Implementation of an Efficient Architecture Using FPGA for Image Processing with a Sliding Window	Fares Saleh Fathi	Dr. Shifa Abdul Rahman	M.Sc.	2009
12	Design and Implementation of a Pipelined Arithmetic Processor Using FPGA	Qutaiba Abdullah	Dr. Basel Shakir Mahmoud	M.Sc.	2009
13	Implementation of a Fuzzy Controller Rule Surface Using FPGA	Mohammed Mahmoud Hussein	Dr. Fakhraddin Hamed	M.Sc.	2009
14	Brain Signal Classification Using Short-Time Fourier Transform Implemented on FPGA	Tuqa Abdul Hameed Fathi	Dr. Abdul Bari Rawuf	M.Sc.	2010
15	Construction of Wavelet Transform Using Interleaved Digital Wavelet Filters	Sama Nizar Mohammed Al-Faidhi	Dr. Jasim Mohammed Abdul Jabbar	M.Sc.	2010
16	Design and Implementation of JPEG/IJPEG Processors	Abdulkarim Mohammed Saleh	Dr. Ahlam Fadhil Mahmoud	M.Sc.	2010
17	Design and Implementation of a SIMD-Type Vector Processor on FPGA	Mamoun Abdul Jabbar	Dr. Basel Shakir Mahmoud	M.Sc.	2010
18	Smart Card System with Iris Recognition	Mohammed Abdul Muttalib	Dr. Fakhraddin Hamed	M.Sc.	2010
19	FPGA-Based Implementation of Single-Tone Sine and Cosine Generators for Signal Analysis and Transmission	Noor Najib Qaqous	Dr. Jasem Mohammed Abdul Jabbar	M.Sc.	2010
20	Implementation of Digital Signature on FPGA Chip	Ahmed Nazem Mohammed	Dr. Abdulilah Abdul Jabbar	M.Sc.	2011

No.	Title	Student Name	Supervisor	Degree	Year
21	Design and Implementation of Control Processing Units on Field Programmable Gate Arrays	Zina Ali Mohammed	Asst. Prof. Thakwan Mohammed Salim	M.Sc.	2011
22	Implementation of an Intelligent Controller for a Mobile Robot on a Single FPGA Chip	Shawkat Sabah Khairallah	Dr. Rafed Ahmed	M.Sc.	2011
23	Brain Tumor Detection and Tissue Segmentation from MRI Images Using a Hybrid Technique	Ameen Mohammed Abdul Salam	Dr. Qutaiba Ibrahim Ali	M.Sc.	2013
24	Reducing Power Consumption in Data Centers Using Artificial Intelligence Technique	Al-Nawwar Jasim Mohammed	Dr. Ahlam Fadhil Mahmoud	M.Sc.	2013
25	Real-Time Statistical Processor Design and Implementation Using FPGA	Sarmad Fakhraddin	Dr. Basel Shakir Mahmoud	M.Sc.	2013

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