

University of Mosul
 Computer Engineering Department
 Subject: Programmable Logic Devices (PLD)
 Class: M.Sc. Course
 Theory (2 hours/week)
 Teaching Language: English

| Article | Week |
|--|------|
| Introduction to Logic Design | 1 |
| Data Representation | 1 |
| IEEE 754_Float Point | 1 |
| Register Transfer Language | 1 |
| Memory | 1 |
| Programmable Logic Design (PLD) | 1 |
| SPLD&CPLD&FPGA Devices | 2 |
| Spartan Family_Part1 | 1 |
| Spartan Family_Part2 | 1 |
| (VHDL)+ISE | 1 |
| (VHDL)+ISE | 1 |
| Seminars: (Zynq-7000 SoC, Vivado Design Suite, System Generator, MicroBlaze Processor, PicoBlaze_Xilinx KCPSM3) | 1 |
| Seminars: (FPGA and LabVIEW, Intellectual Property (IP) core in FPGA, What is a FIFO in the FPGA, Xilinx 7-series family, ZTEX FPGA Boards with Open Source SDK) | 1 |
| Seminars: (Altera fpga devices, Xilinx ChipScope, AC701 Evaluation Board for the Artix-7 FPGA, Verilog Language, How to use the memory in VHDL) | 1 |

Text book1: M. Mano "Computer System Architecture"