

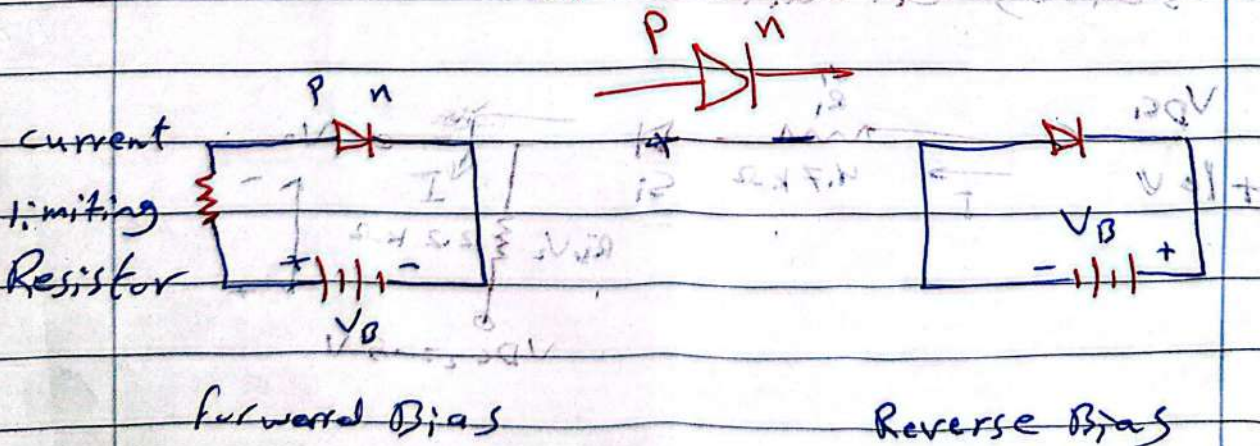
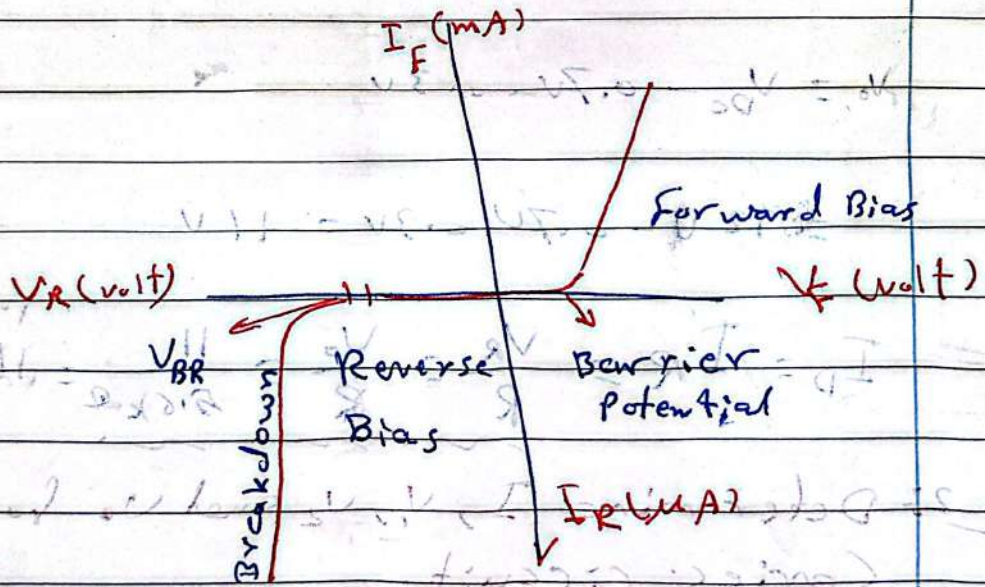
Ch. 2

Diodes and Applications

2.1 The Diode characteristic Curve

in forward bias There is very little forward current below barrier potential

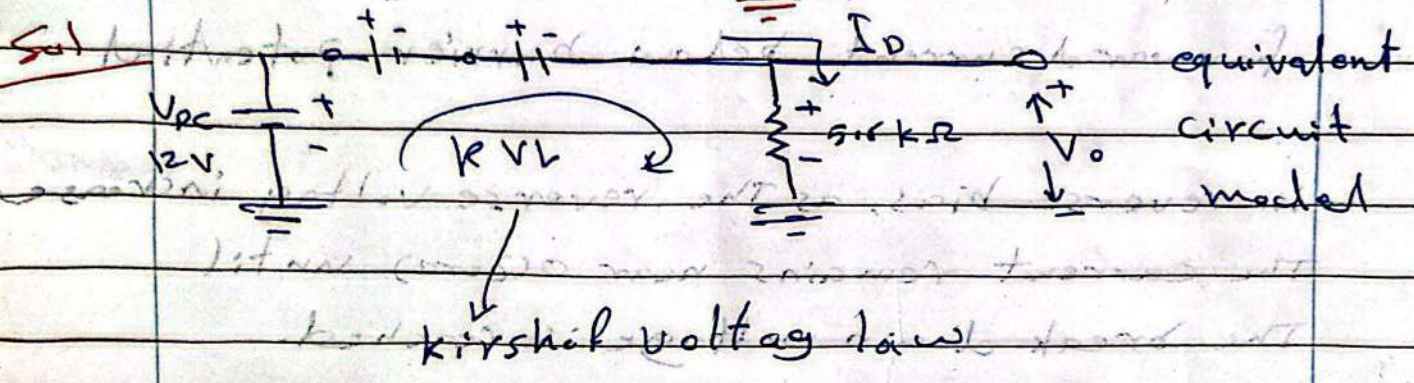
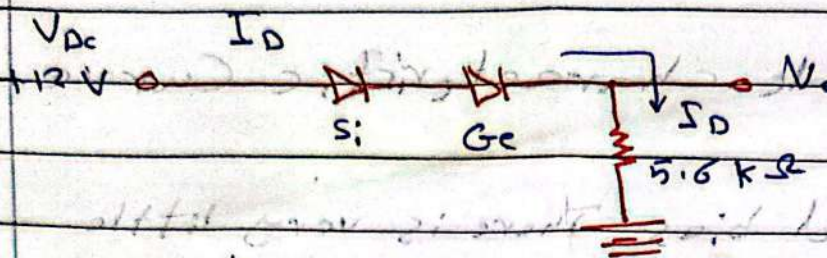
in reverse bias, as the reverse voltage increases the current remains near 0 (zero) until the break down voltage is reached.



①
②

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Ex 1: Determine $V_{out}(V_o)$ and I_D for The Series circuit:

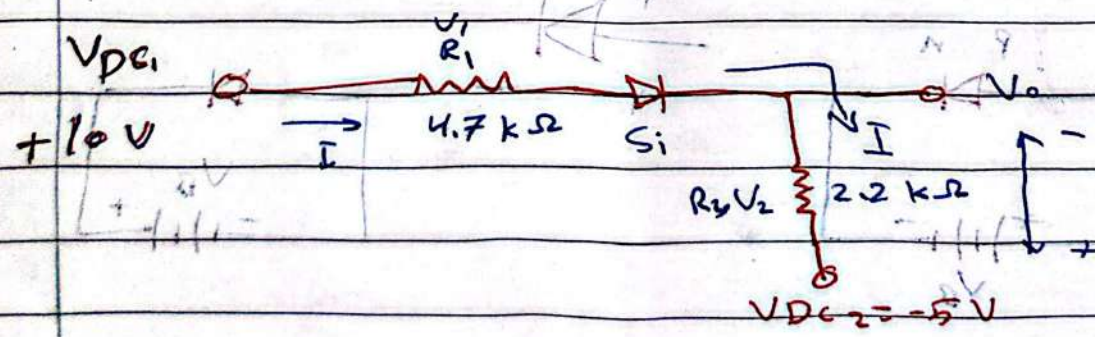


$$V_o = V_{DC} - 0.7V - 0.3V$$

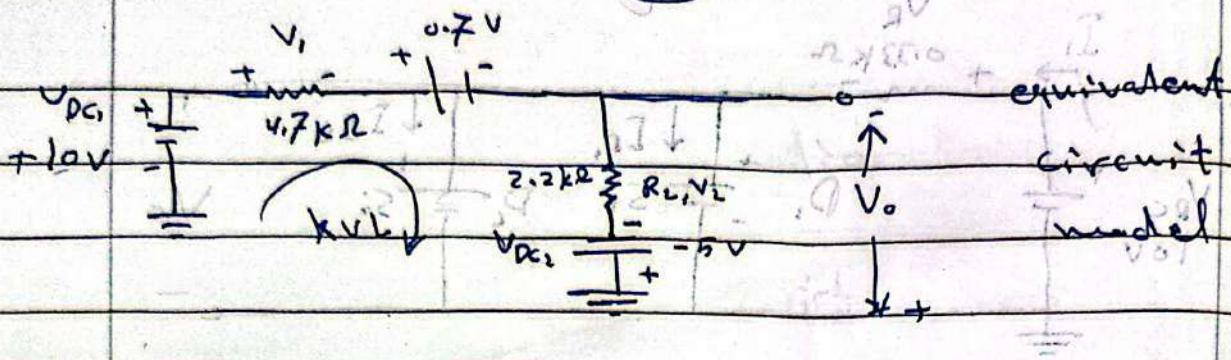
$$= 12V - 0.7V - 0.3V = 11V$$

$$I_D = I_R = \frac{V_R}{R} = \frac{V_o}{R} = \frac{11V}{5.6k\Omega} = 1.96 \text{ mA}$$

Ex 2: Determine I , V_1 , V_2 and V_o for The Series circuit.



(5)



$$I = \frac{V_{DC1} + V_{DC2} - 0.7}{R_1 + R_2} = \frac{10V + 5V - 0.7}{4.7k\Omega + 2.2k\Omega}$$

$$I = \frac{14.3V}{6.9k\Omega} = 2.07mA$$

$$V_1 = IR_1 = (2.07mA)(4.7k\Omega) = 9.73V$$

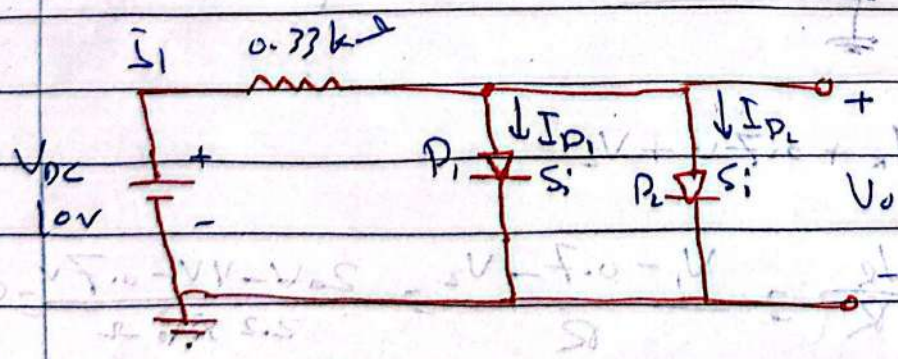
$$V_2 = IR_2 = (2.07mA)(2.2k\Omega) = 4.55V$$

Applying KVL

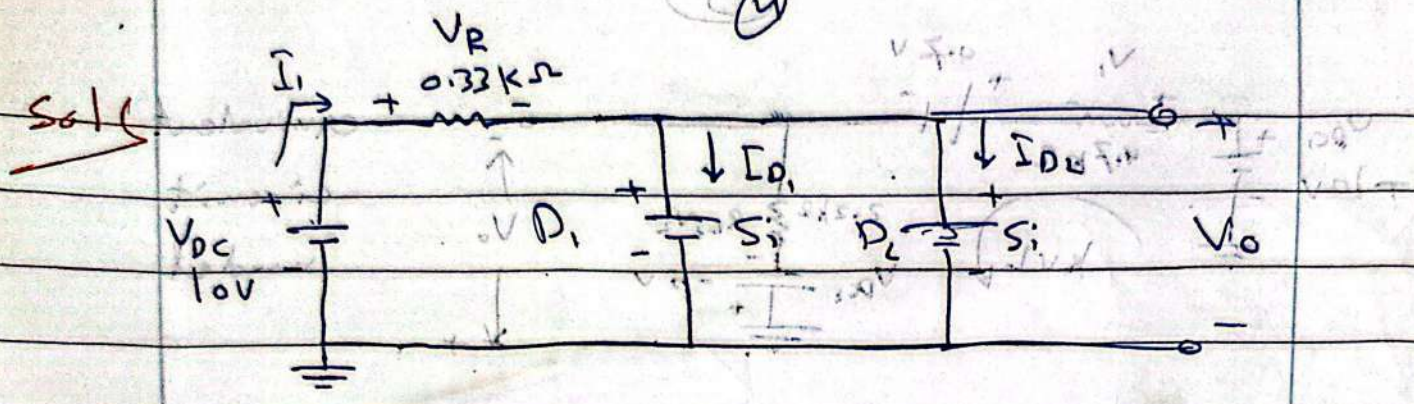
$$V_{DC2} + V_2 - V_0 = 0 \Rightarrow V_0 = V_2 - V_{DC2}$$

$$V_0 = 4.55V - 5V = -0.45V$$

EX)3: Determine V_0 , I_1 , I_{D1} , and I_{D2} for the parallel diode configuration.



(4)

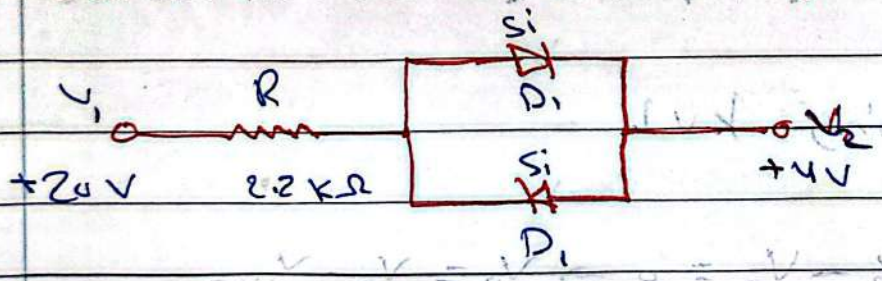


$$V_0 = 0.7 + 0.7 = 1.4 \text{ V}$$

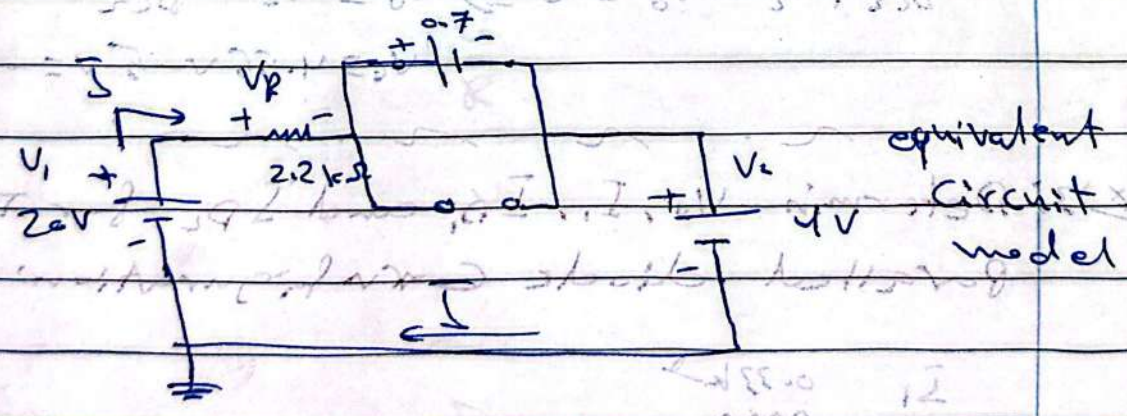
$$I_1 = \frac{V_R}{R} = \frac{V_{DC} - 0.7}{0.33 \text{ k}\Omega} = \frac{10 - 0.7}{0.33 \text{ k}\Omega} = 28.18 \text{ mA}$$

$$I_{D1} = I_{D2} = \frac{I_1}{2} = \frac{28.18 \text{ mA}}{2} = 14.09 \text{ mA}$$

Ex 4 Determine the current I for the network



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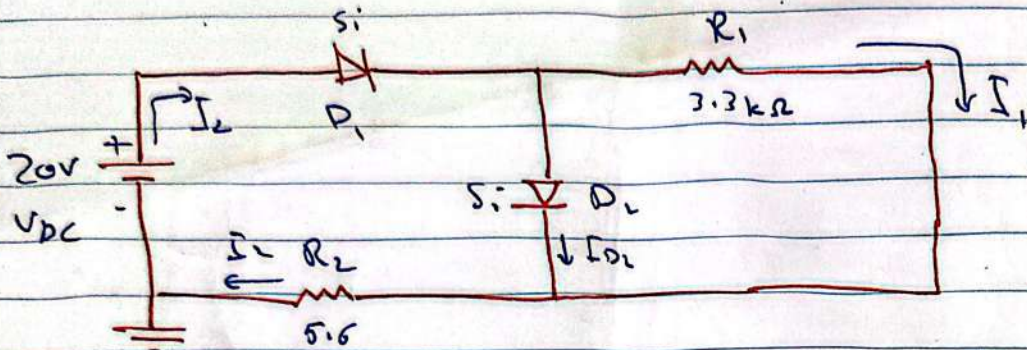


$$V_1 = V_R + 0.7 \text{ V} + V_2$$

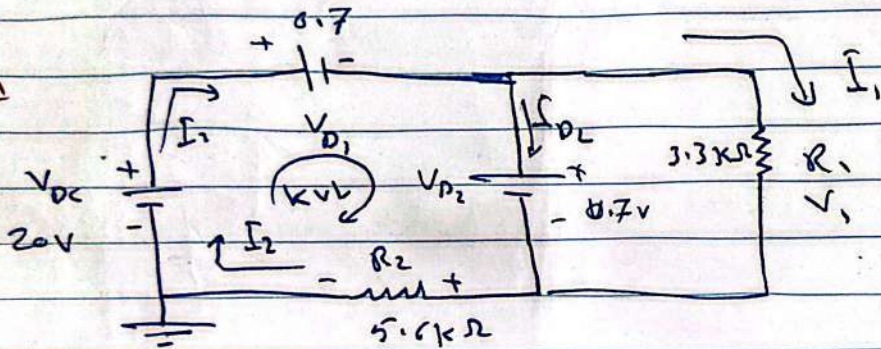
$$I = \frac{V_R}{R} = \frac{V_1 - 0.7 - V_2}{R} = \frac{20 \text{ V} - 4 \text{ V} - 0.7 \text{ V}}{2.2 \times 10^3 \Omega} = 9.65 \text{ mA}$$

(5)

Ex) Determine the currents I_1 , I_2 and I_{D_1} , I_{D_2} for the net work



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$$I_1 = \frac{V_{D_2}}{R_1} = \frac{0.7 \text{ V}}{3.3 \text{ k}\Omega} = 0.212 \text{ mA}$$

$$V_{DC} = V_2 + V_{D_1} + V_{D_2} \Rightarrow V_2 = V_{DC} - V_{D_1} - V_{D_2}$$

$$V_2 = 20 \text{ V} - 0.7 \text{ V} - 0.7 \text{ V}$$

$$V_2 = 18.6 \text{ V}$$

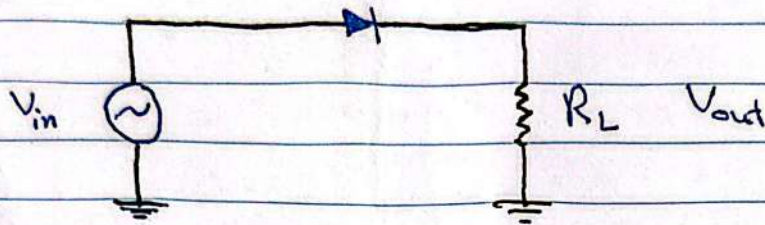
$$I_2 = \frac{V_2}{R_2} = \frac{18.6 \text{ V}}{5.6 \text{ k}\Omega} = 3.32 \text{ mA}$$

$$I_{D_2} = I_2 - I_1 = 3.32 \text{ mA} - 0.212 \text{ mA} = 3.108 \text{ mA}$$

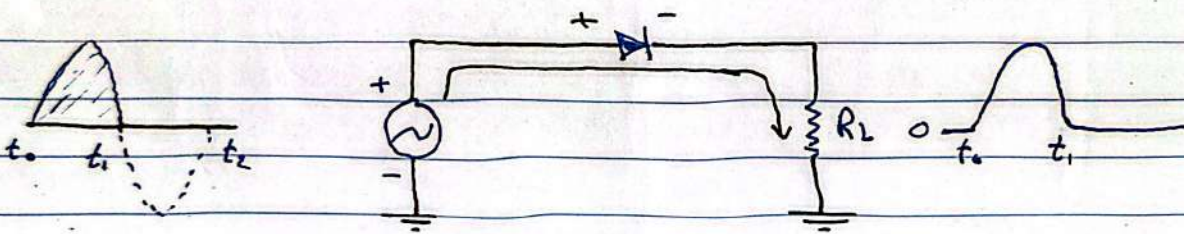
Rectifiers ①

~~Half-wave Rectifier~~

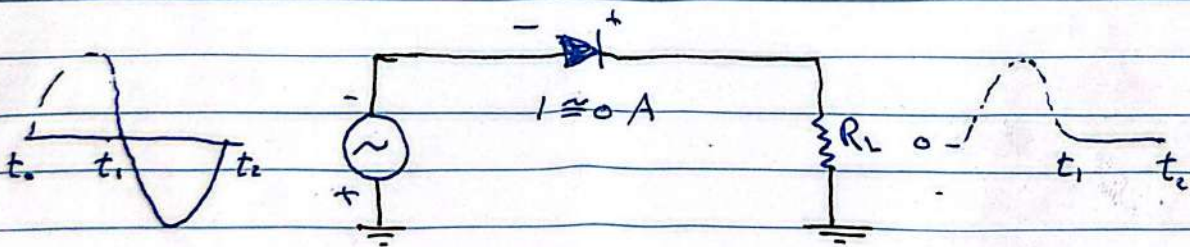
Because of their unique ability to conduct in only one direction, diodes are used in rectifier circuits. Rectification is the process of converting ac to pulsating dc.



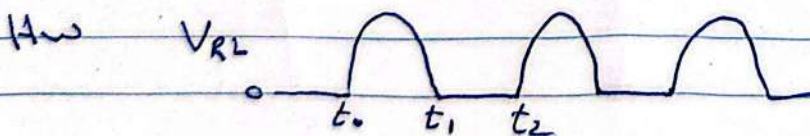
A. Half-wave rectifier circuit



B. Operation during positive alternation



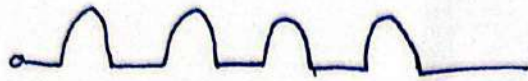
C. Operation during negative alternation



D. Half-wave output voltage

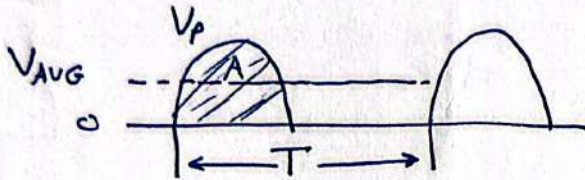
(7)

$$V_p = 100\text{V}$$



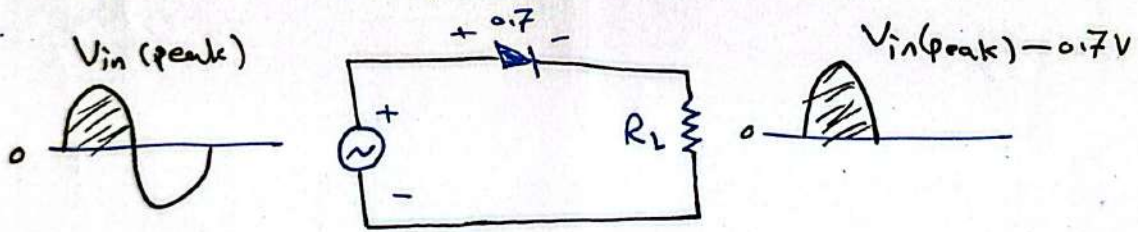
$$V_{dc} = V_{AVG} = \frac{V_p}{\pi} = \frac{100\text{V}}{\pi} = 31.83\text{V}$$

* The average value is the value that would be indicated by a dc volt meter.



Average value of half-wave rectified signal.

* Effect of Barrier potential on half-wave Rectifier output.

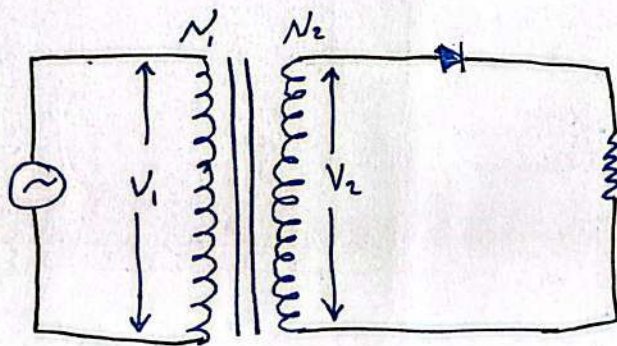


$$V_p(\text{out}) = V_p(\text{in}) - 0.7\text{V}, \text{ for silicon.}$$

$$V_p(\text{out}) = V_p(\text{in}) - 0.3\text{V}, \text{ for germanium.}$$

2.3 :- Half-wave Rectifier^⑧ with Transformer - Coupled Input.

- * Transformer allows the ¹¹⁰ source voltage to be stepped up or stepped down as needed.
- * Transformer electrically isolated ac power source ~~is~~ from rectifier circuit. Thus ^{is} reducing shock ^{is} hazard.

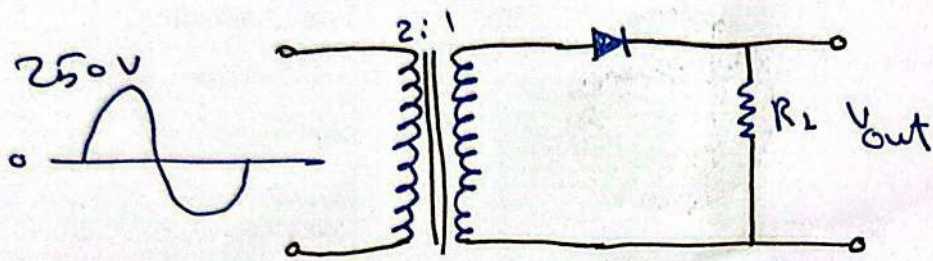


$$V_2 = \left(\frac{N_2}{N_1} \right) V_1$$

- if $N_2 > N_1$, the primary voltage is less than the secondary voltage.
- if $N_2 < N_1$, the primary voltage is greater than the secondary voltage.

9

ex) Determine The peak value of The output voltage.



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$$\frac{N_2}{N_1} = \frac{1}{2} = 0.5$$

$$V_2 = \left(\frac{N_2}{N_1}\right) V_1$$

$= 0.5 (250V) = 125V$, is The secondary peak voltage is

~~to calculate~~ ^{determinet} the peak rectified output voltage is .

$$V_{p(out)} = 125V - 0.7V$$
$$= 124.3V$$